

METHOD AND SYSTEM FOR DESKEWING  
PARALLEL BUS CHANNELS TO INCREASE  
DATA TRANSFER RATES

Inventors: Ejaz Ul Haq, et al.  
Attorney Docket No.: 44176.00036

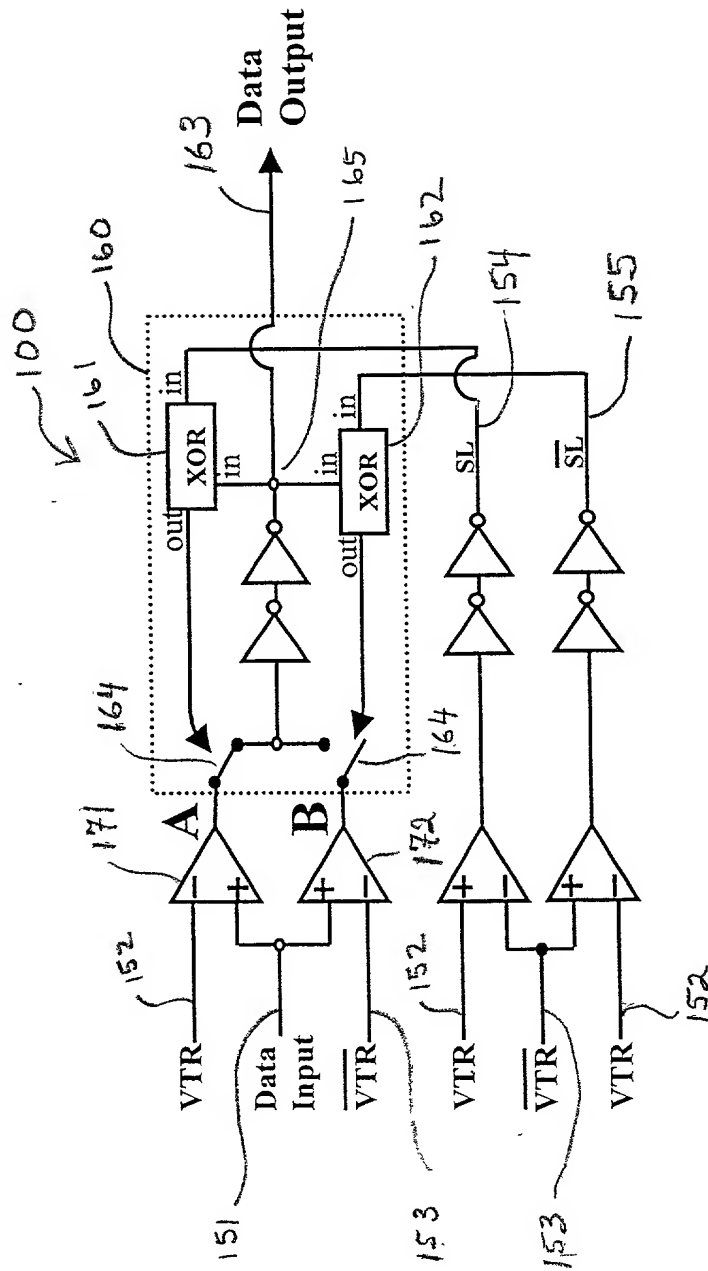


FIG. 1A

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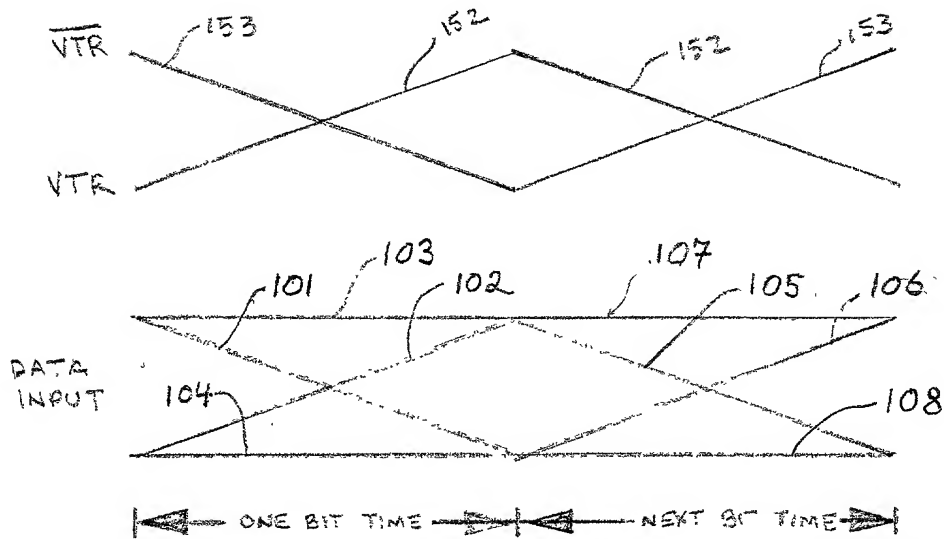


FIG. 1B

20250001-46990001

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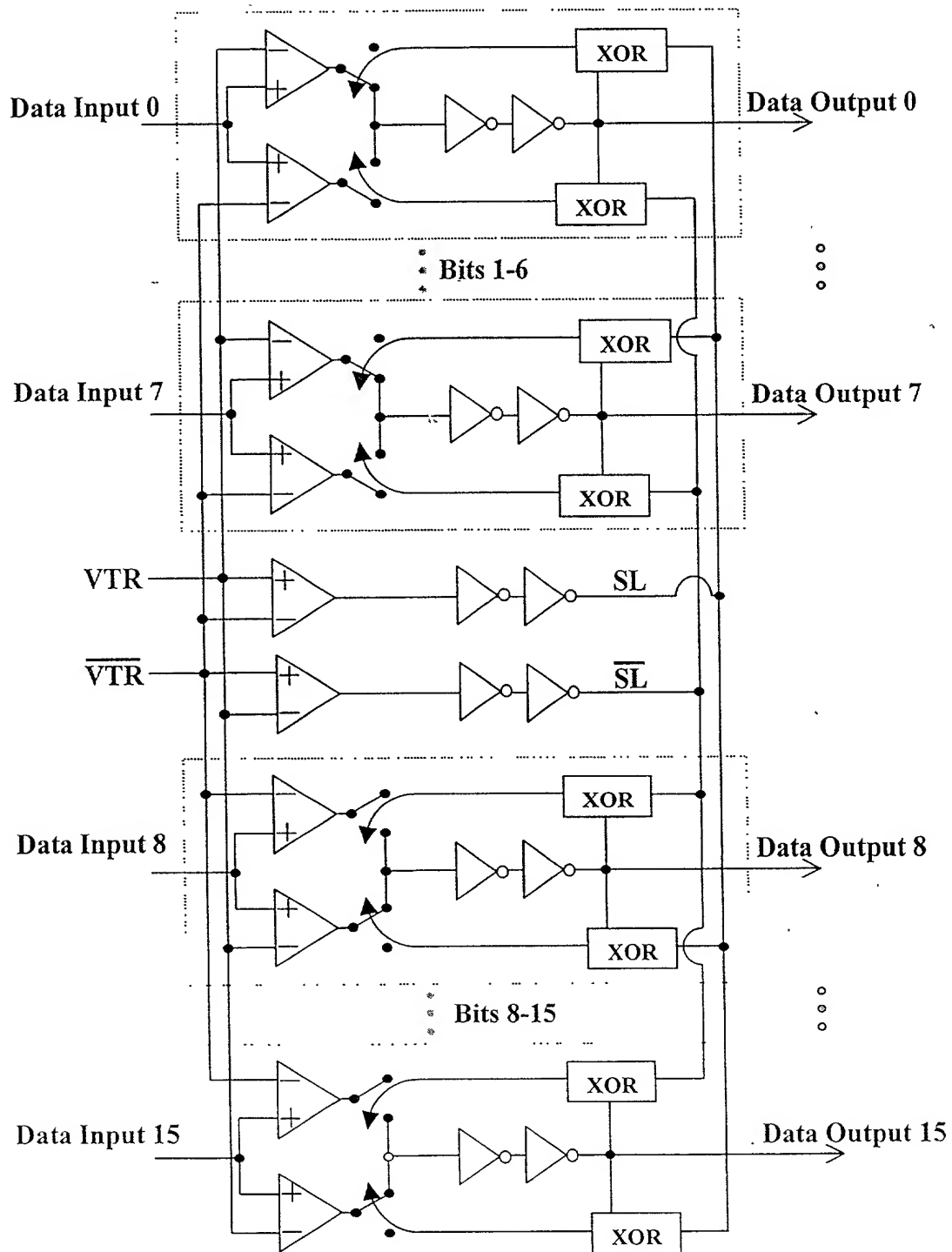


FIG. 2

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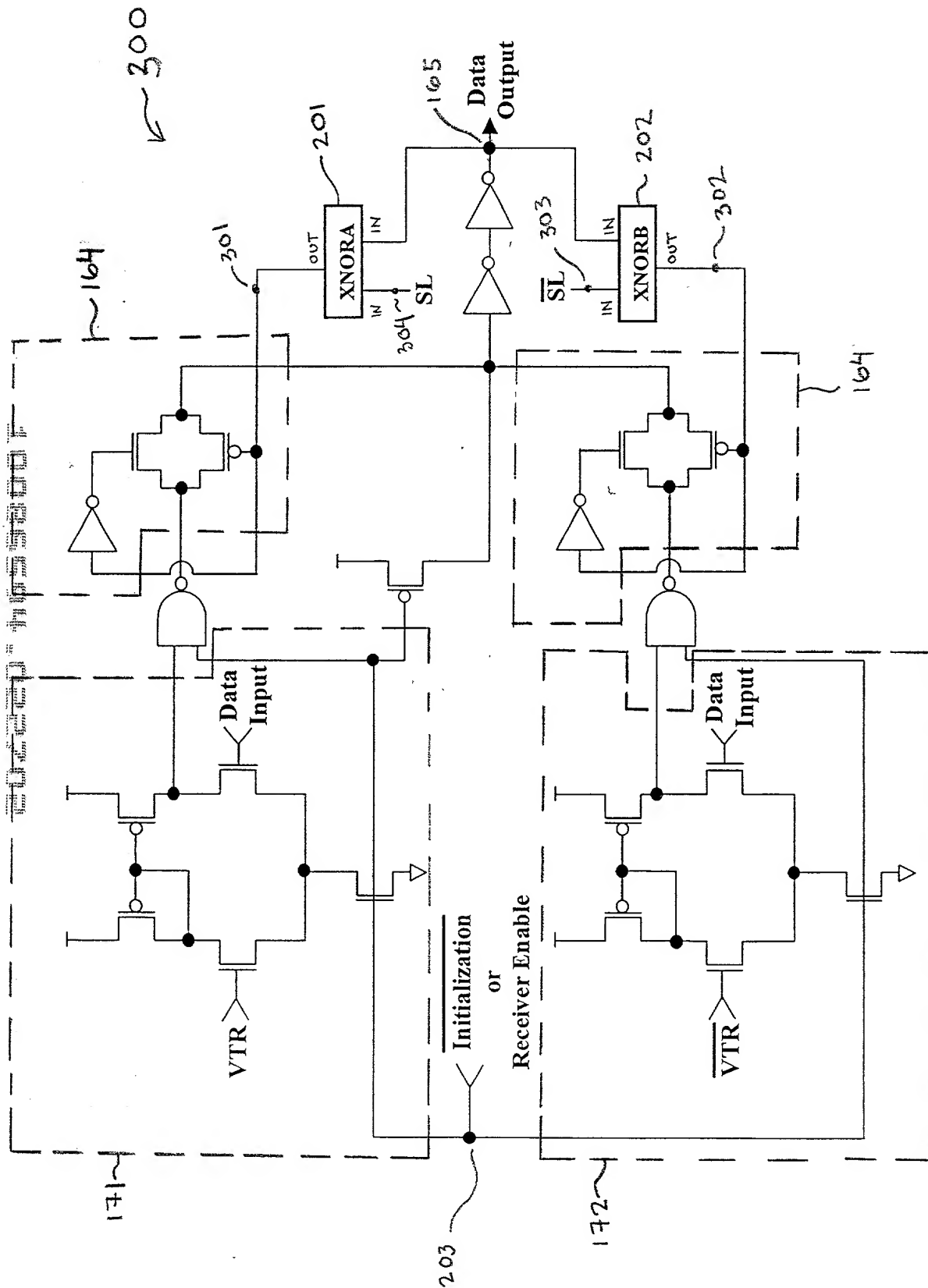
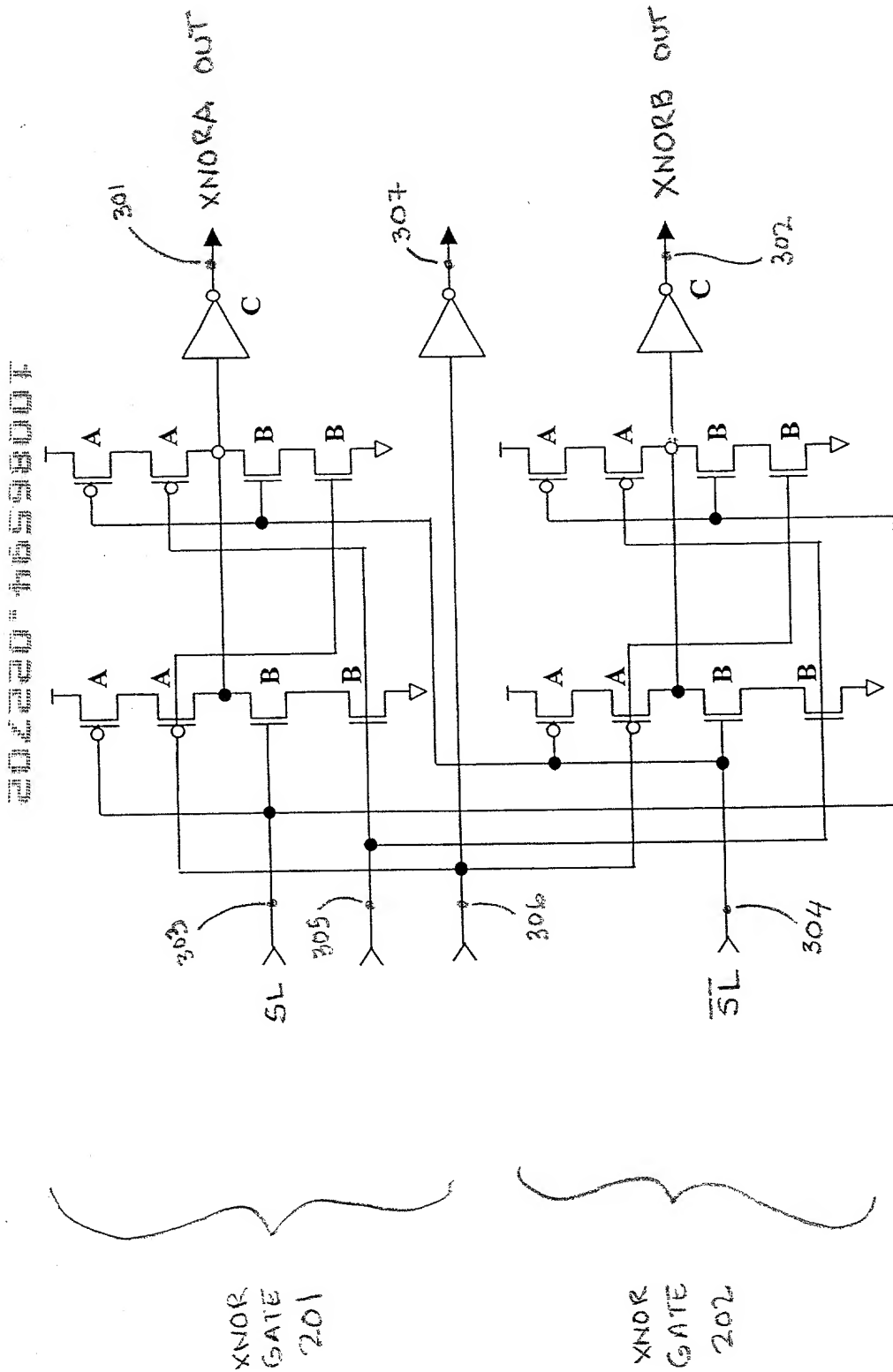


FIG. 3

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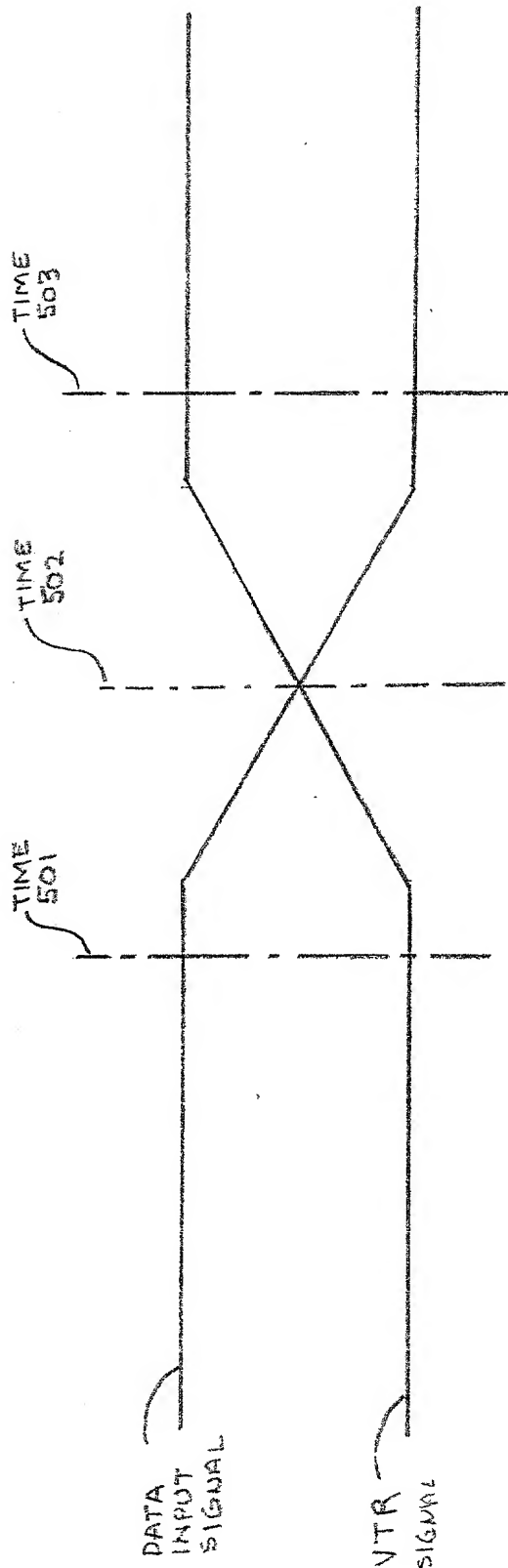


FIG. 5A

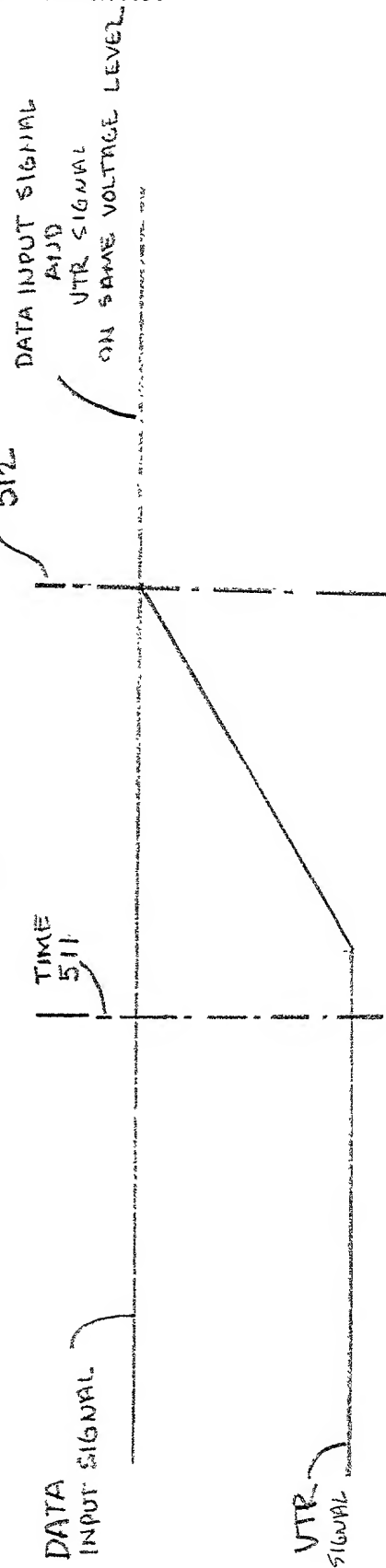


FIG. 5B

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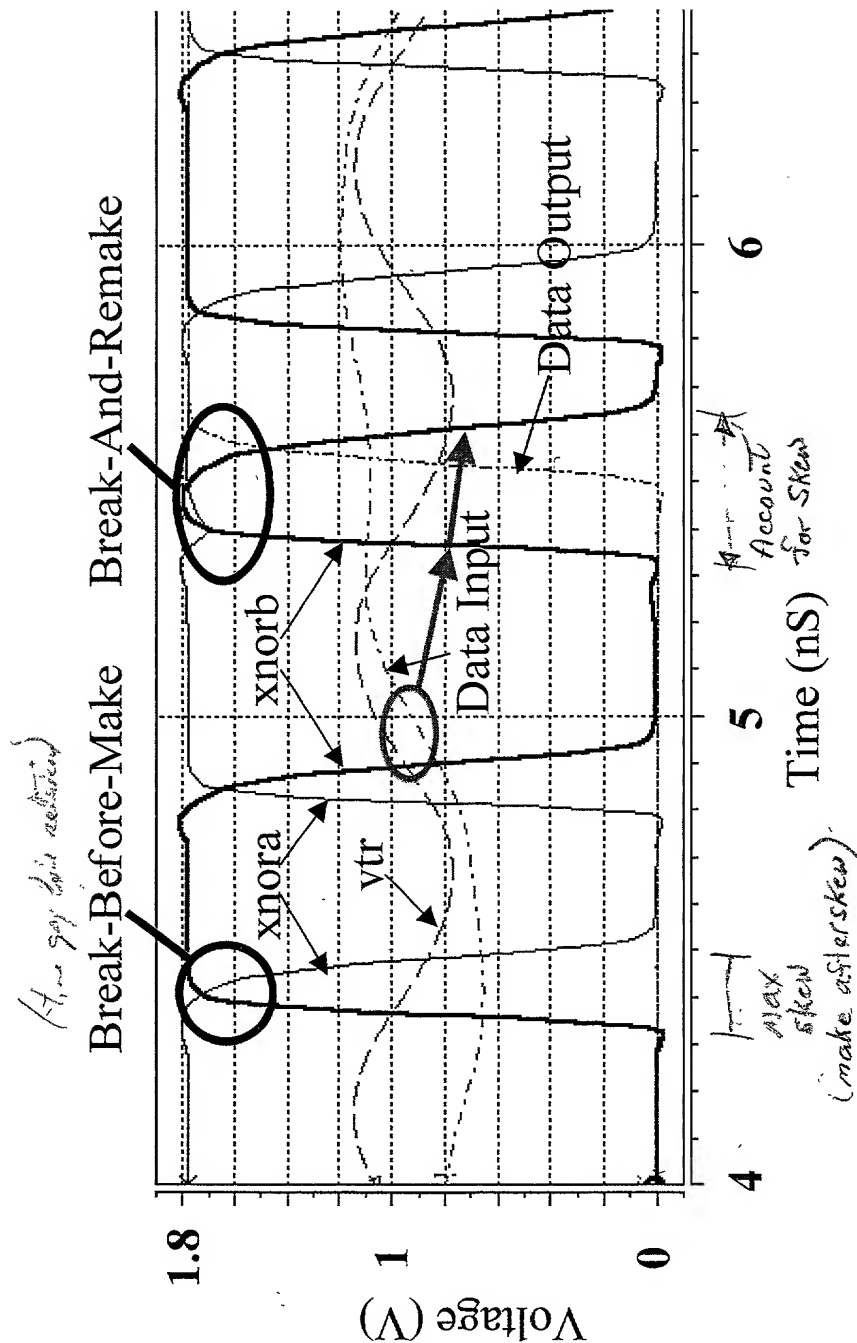


FIG. 5c  
(Replacing xOR with xNOR)

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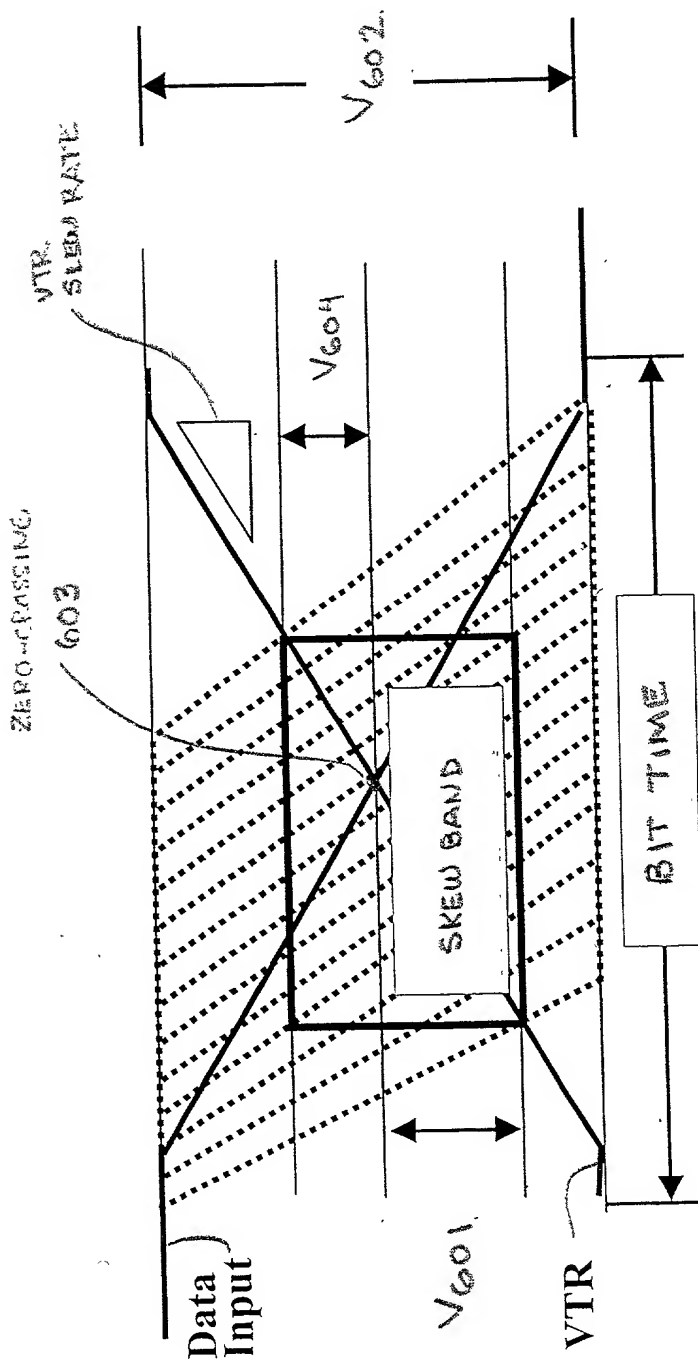
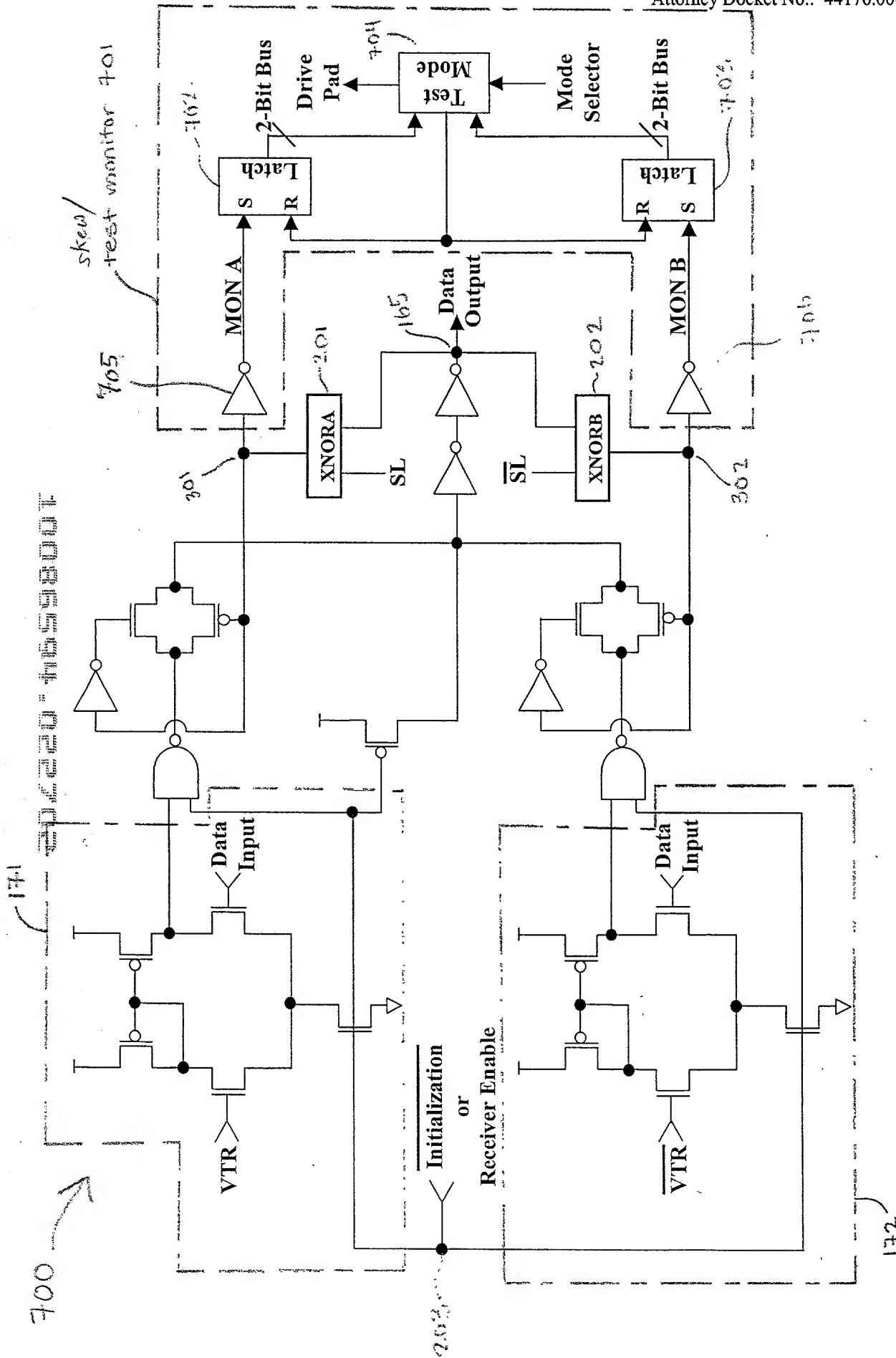


FIG. 6





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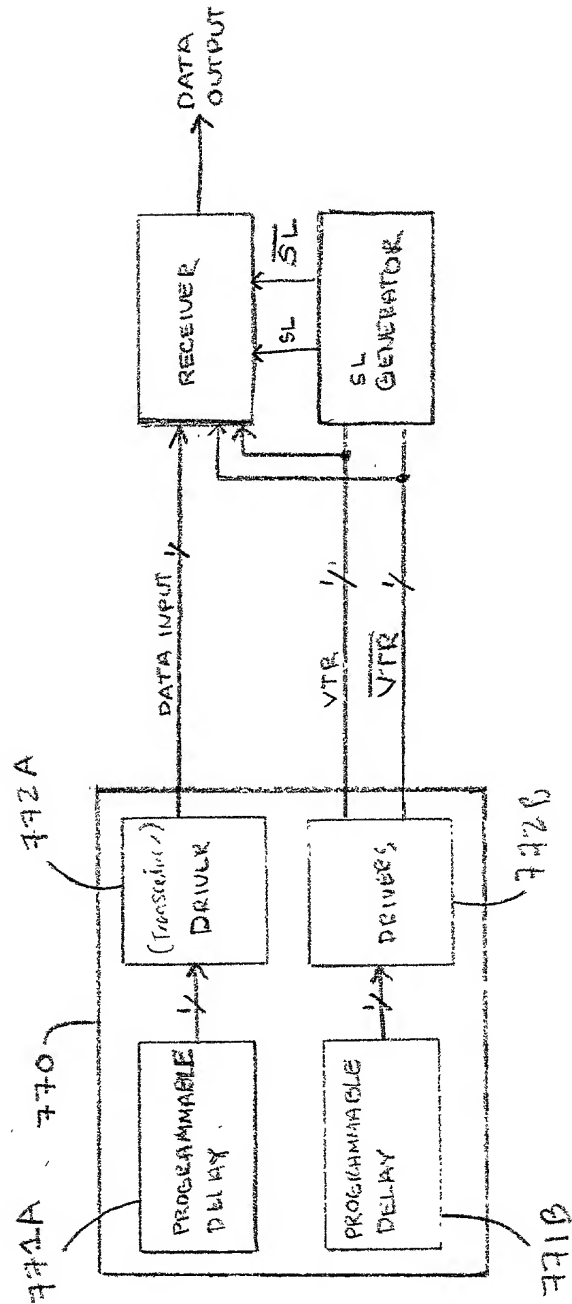
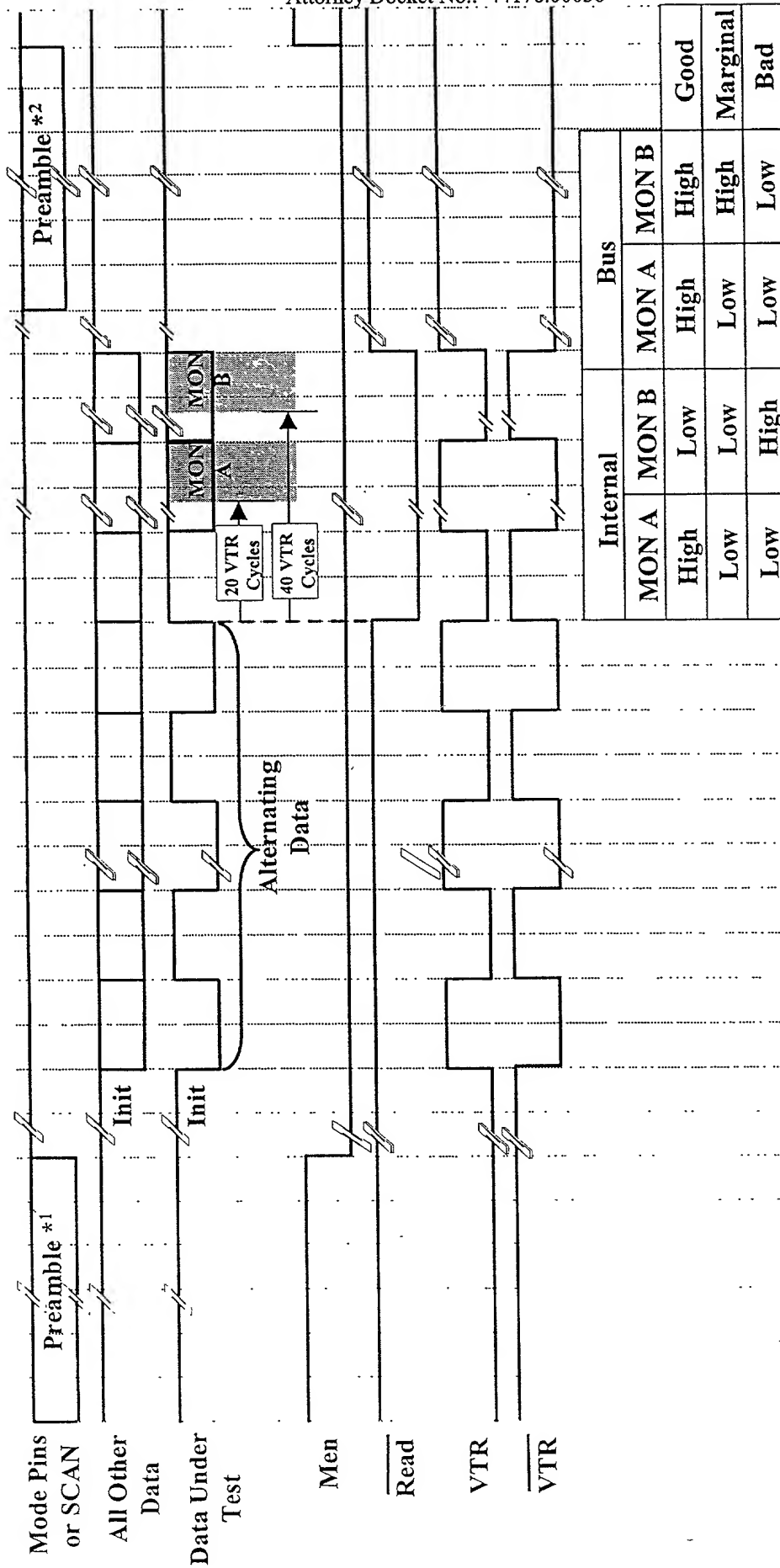


FIG. 7B

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Note \*1: Preamble used to setup test mode

Note \*2: Preamble used to setup next test mode, terminate test mode or set registers

FIG. 8A

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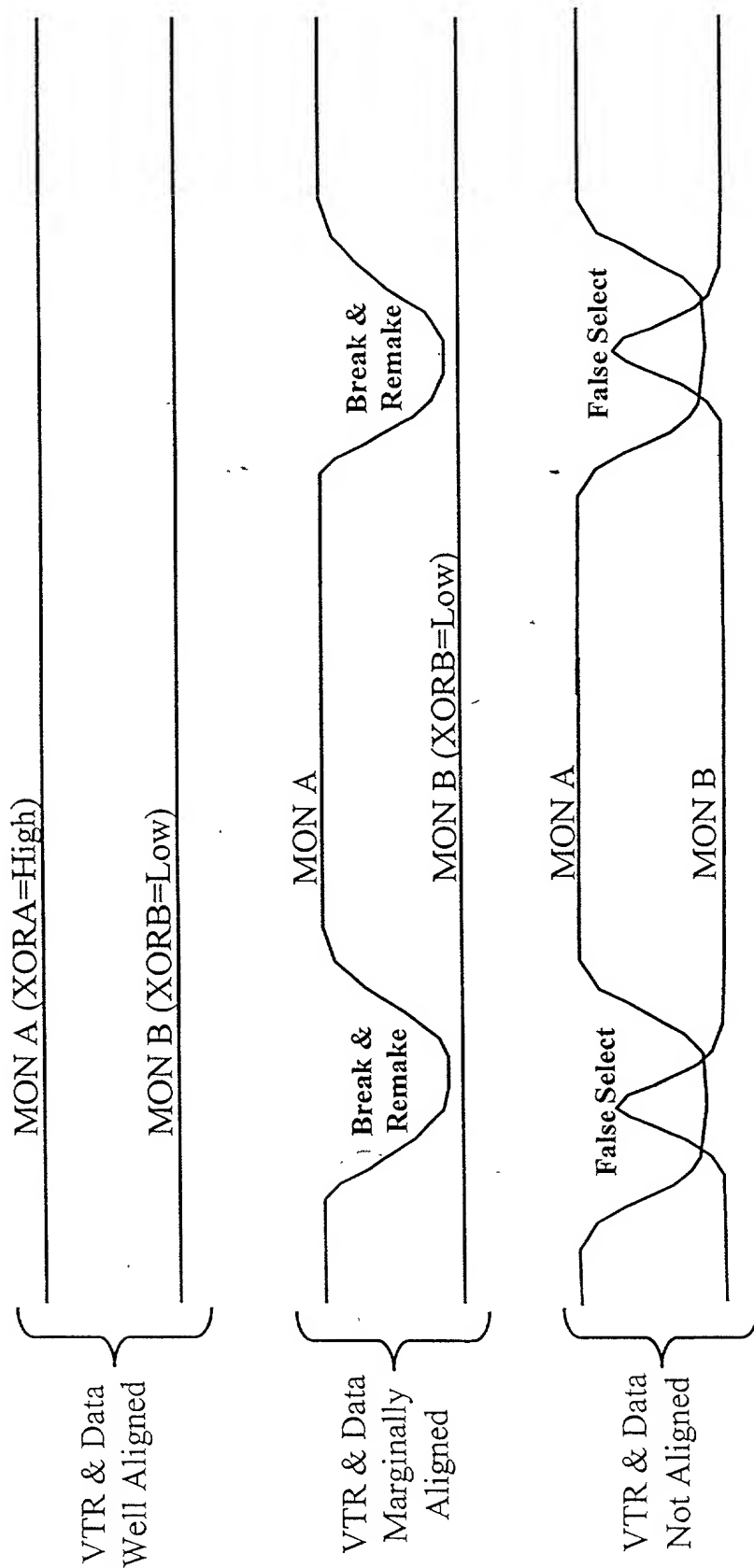


FIG. 8B

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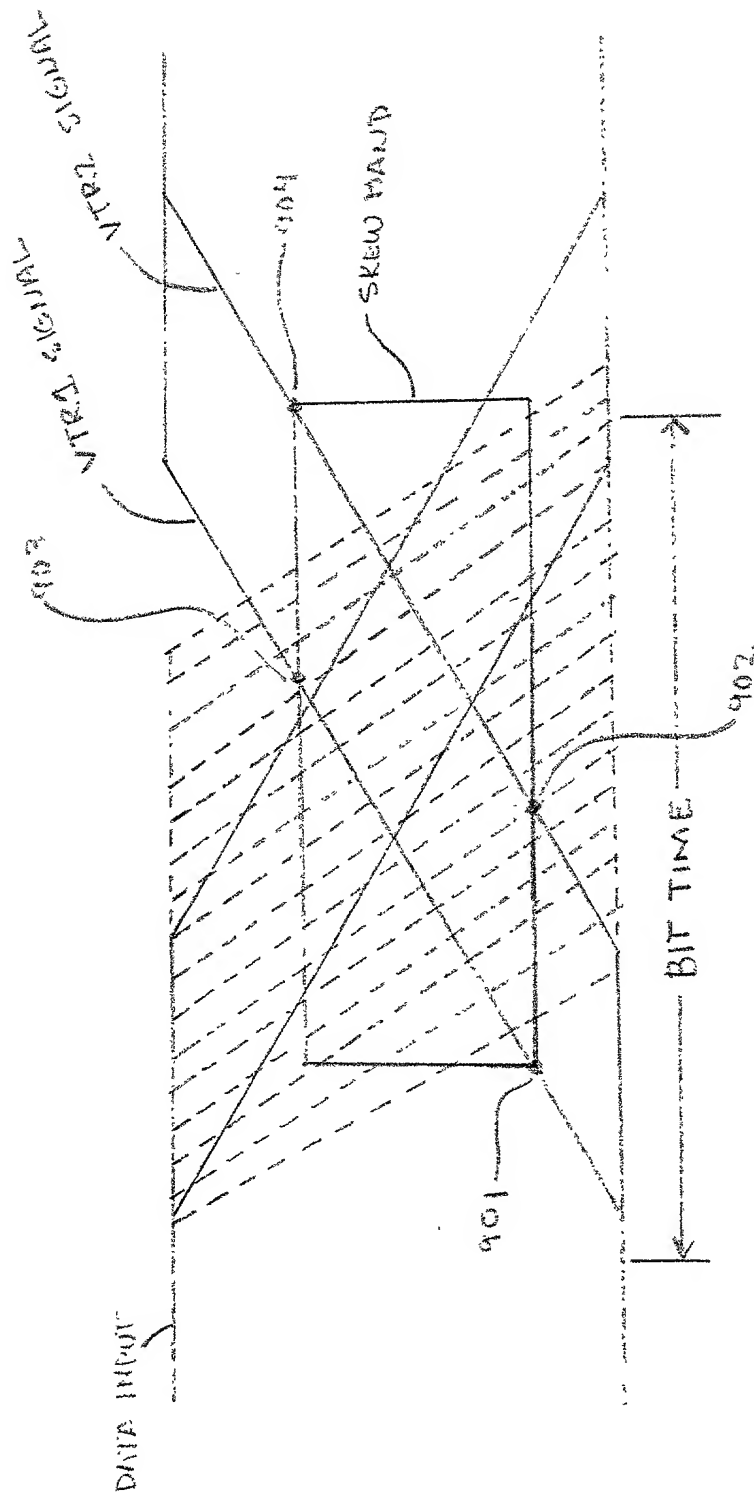


FIG. 9

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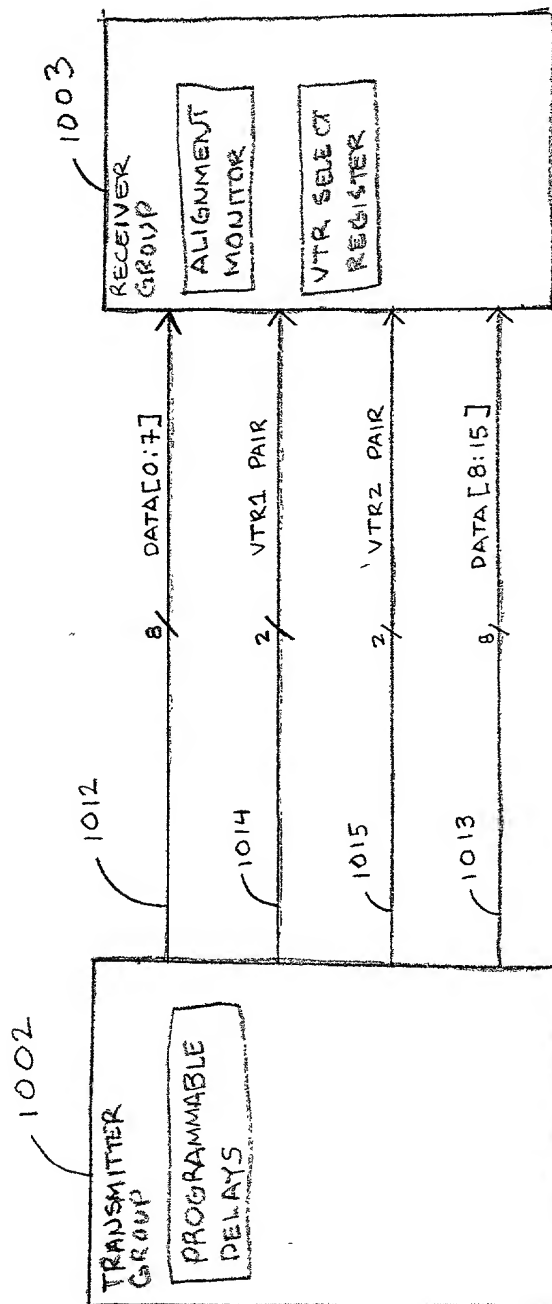


FIG. 10

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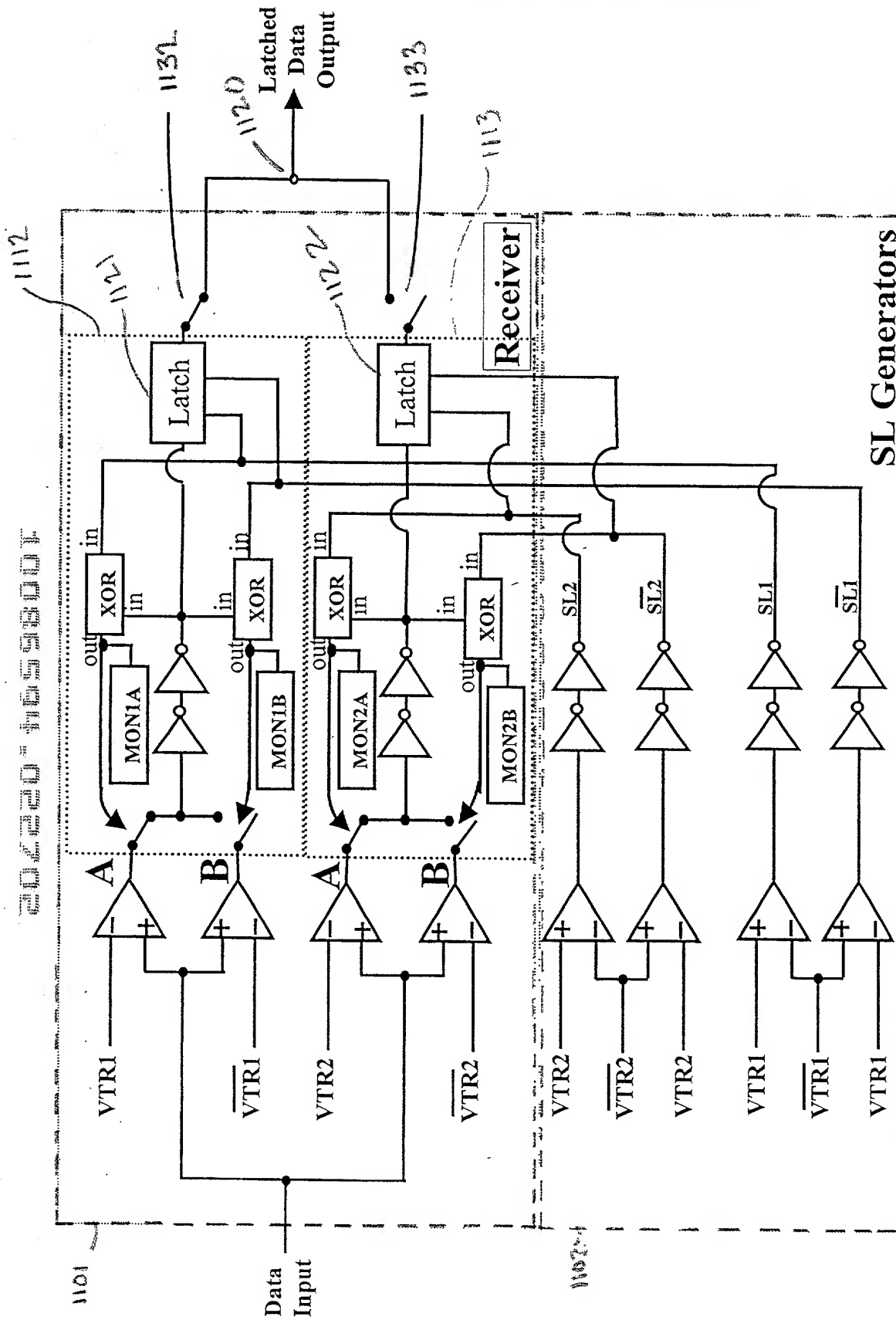


FIG. 11

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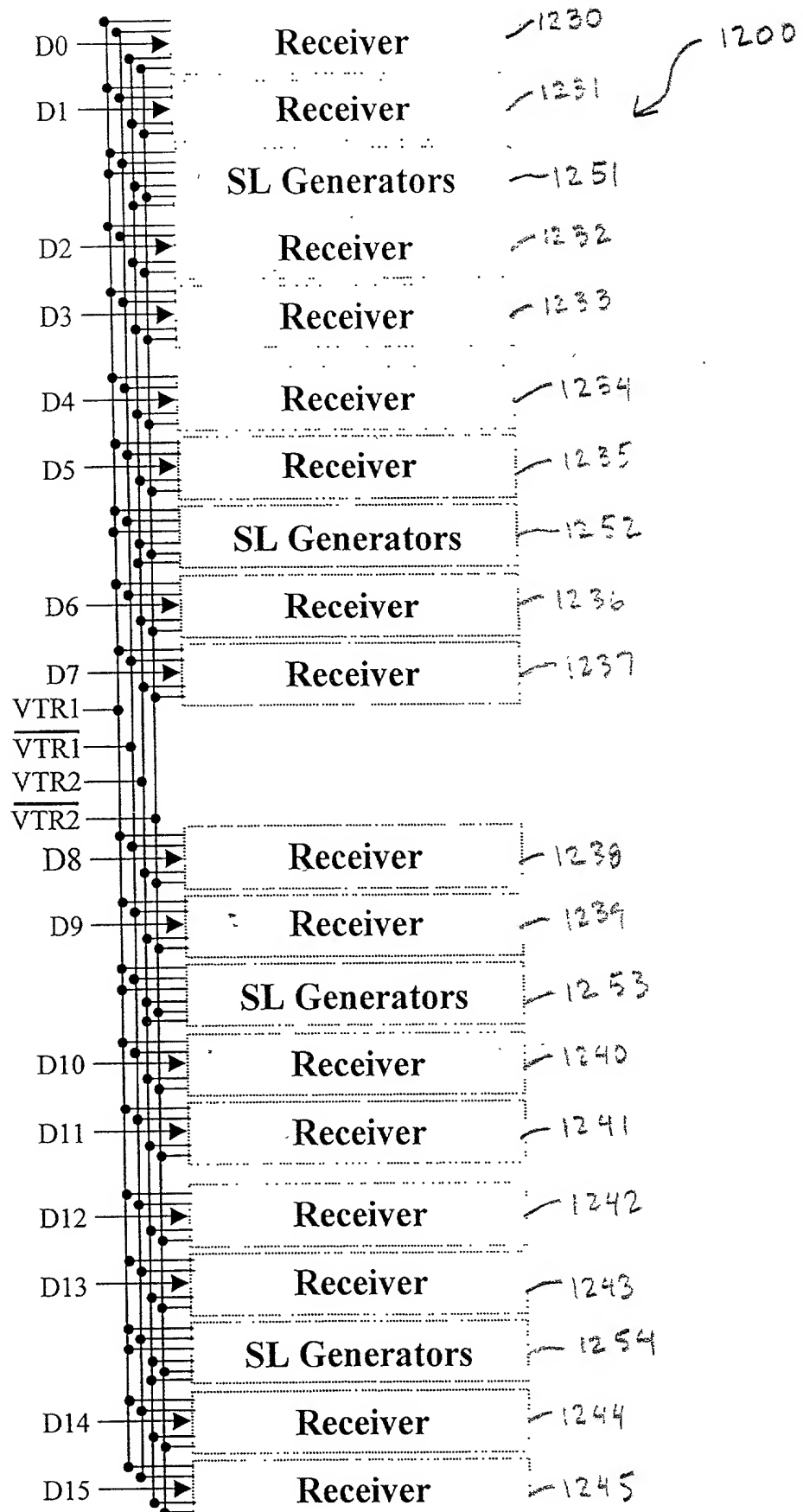


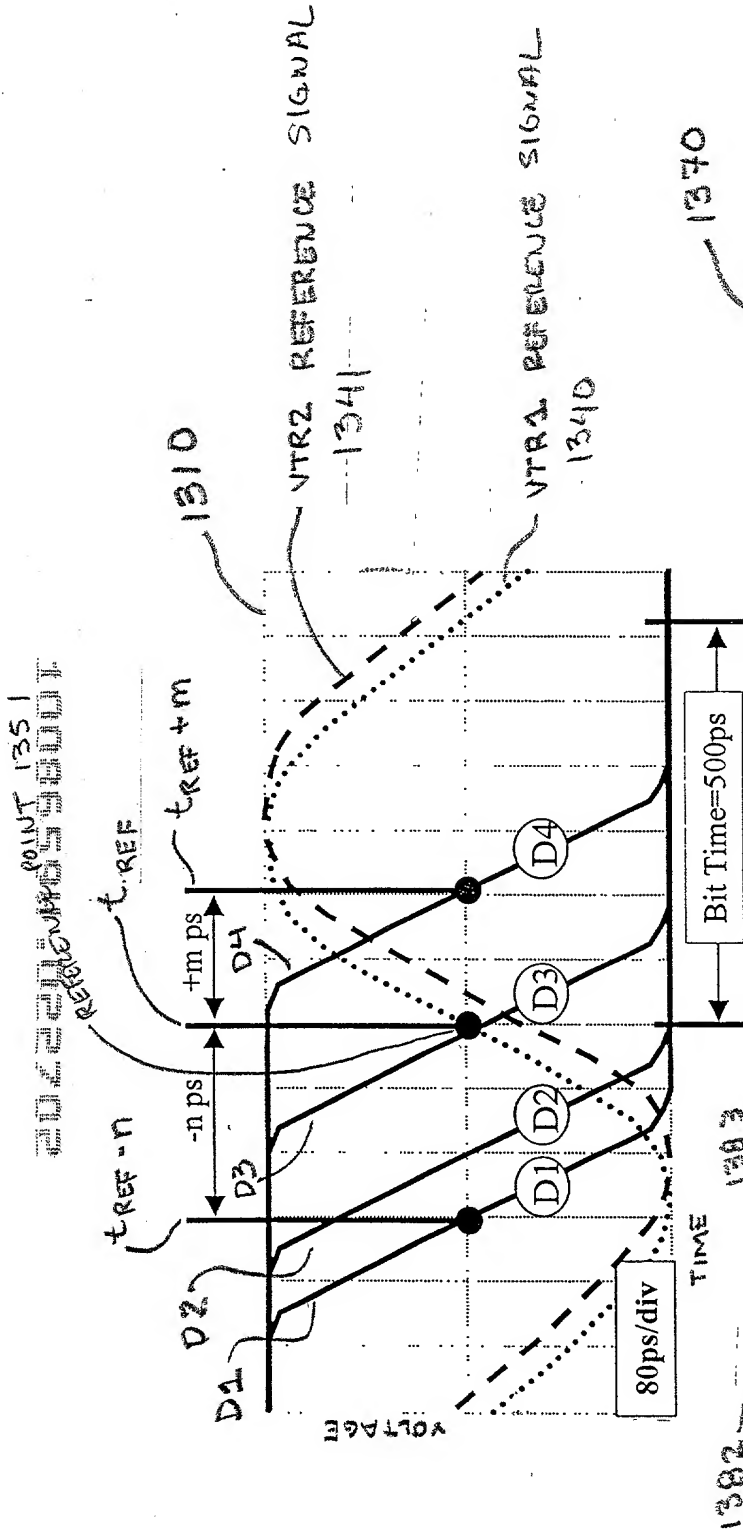
FIG. 12

1008554-02302



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Results												
VTR2=VTR1+40ps	D1-80ps	D1	D1+80ps	D2	D2+80ps	D3	D3+80ps	D4	D4+80ps	D4+80ps	D4+80ps	D4+80ps
VTR1 REF -160ps		Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass
VTR2 REF -160ps		Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass
VTR1 REF -80ps		Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass
VTR2 REF -80ps		Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass
VTR1 REF												
VTR2 REF												
VTR1 REF +80ps												
VTR2 REF +80ps												
VTR1 REF +160ps												
VTR2 REF +160ps												

FIG. 13

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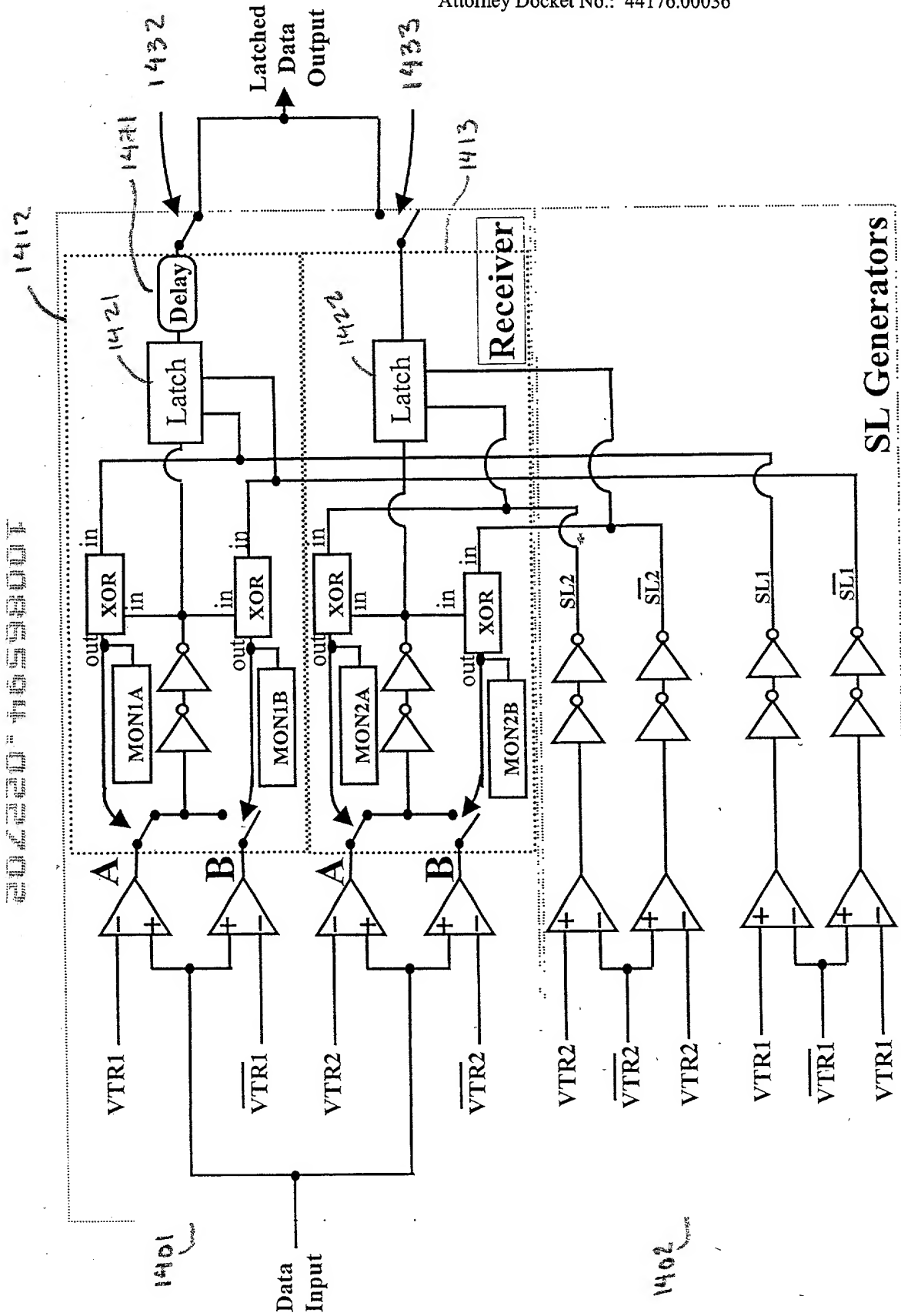


FIG. 14

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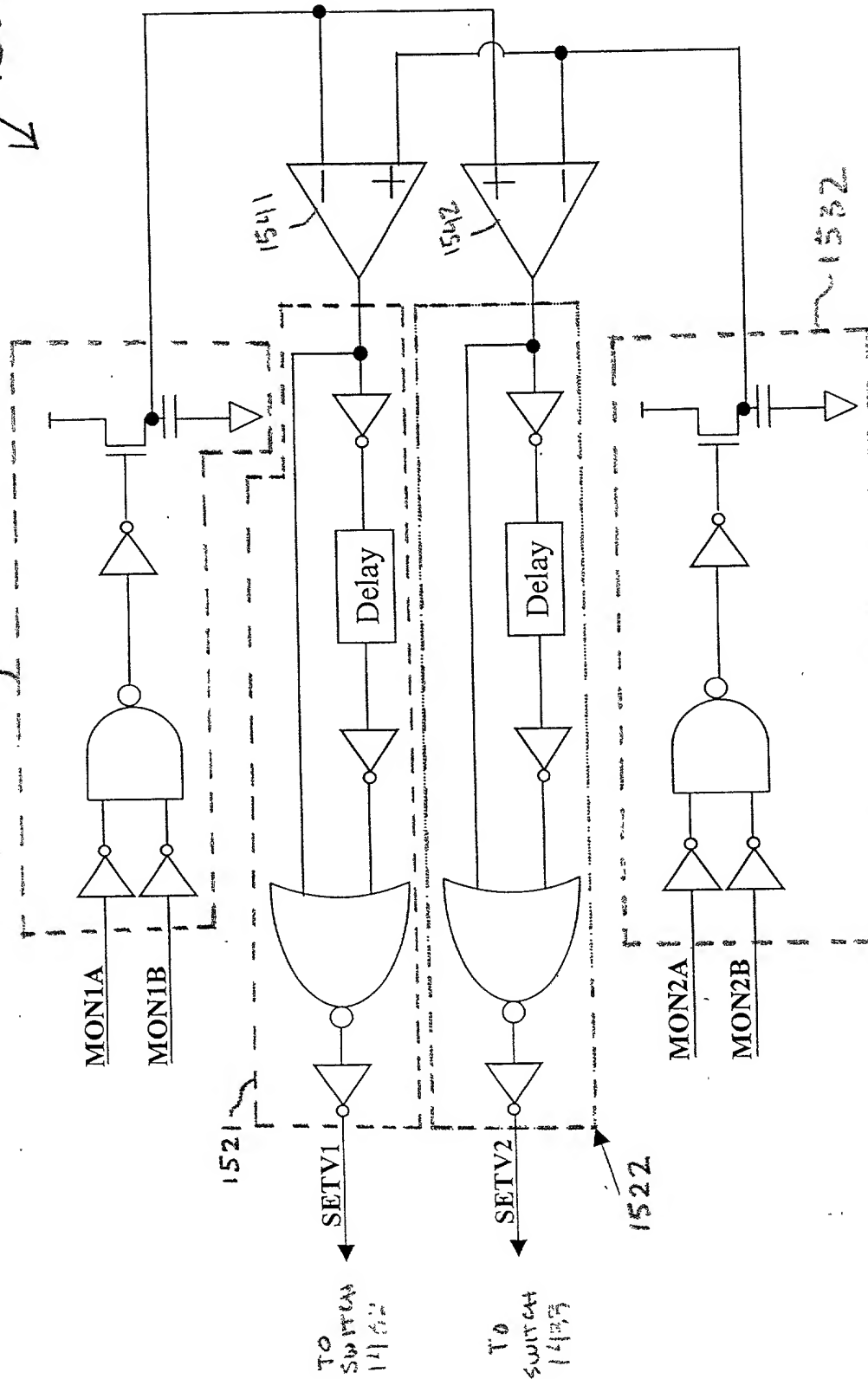


FIG. 15